

Abstract of the Disclosure

A read clock circuit for a disk drive includes a phase-locked loop/voltage controlled oscillator (VFO/PLL) and a frequency synthesizer. The VFO/PLL receives a servo sector transition signal that is related to detected transitions in a servo sector field of a magnetic recording disk, and outputs a servo frequency signal that is synchronous to the servo sector transition signal. The frequency synthesizer receives the servo frequency signal and generates the read clock signal that is synchronous with the servo frequency signal. In a banded recording disk drive the frequency synthesizer generates a unique read clock signal for each data band.